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ATTORNEY DOCKET NO. 10021250-01



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Paul L. Corredoura, et al.

Serial No.: 10/814472

Examiner:

Filing Date: Mar 29, 2004

Group Art Unit: 2631

Title: Digital Monitor Employing A Polyphase Up-Converter Structure

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

(a)  Under 37 CFR 1.97(b).  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first Office action on the merits; whichever occurs last).

(b)  Under 37 CFR 1.97(c) together with **either** a:  
 Statement under CFR 1.97(e), or  
 \$180.00 fee under 37 CFR 1.17(p).  
(After the CFR 1.97(b) time period, but before a final action or notice of allowance, whichever occurs first).

(c)  Under 37 CFR 1.97(d) together with: a  
Statement under 37 CFR 1.97(e), **and**  
\$180.00 fee as set forth in 37 CFR 1.17(p).  
(After a final action or notice of allowance, whichever occurs first, but before payment of the issue fee).

STATEMENT UNDER 37 CFR 1.97(e)

The undersigned certifies that:

Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement, or  
 No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

PRIOR APPLICATIONS

References identified with an asterisk (\*) in the enclosed PTO Form 1449, were disclosed in prior Patent Application No. , filed , now U.S. Patent No. , and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

FOREIGN LANGUAGE DOCUMENTS

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56(c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

FEE AUTHORIZATION

Please charge to Deposit Account **50-1078** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

I hereby certify that this correspondence is being deposited with the United States Postal Service as:

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OR

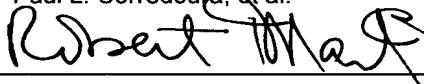
I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

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Respectfully submitted,

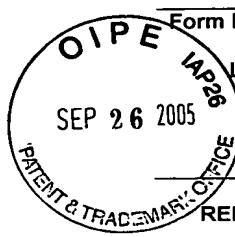
By   
Paul L. Corredoupe, et al.

Robert T Martin  
Attorney/Agent for Applicant(s)

Reg. No. 32,426

Date: **September 21, 2005**

Telephone No. 650 485 7533



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Form PTO-1449

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

**ATTORNEY DOCKET NO.**  
**10021250-01**

**SERIAL NO.**

**APPLICANT**

Paul L. Corredoura, et al.

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**FILING DATE**

PRINTING DATE

GROUP  
2631

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENT

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

	Johan Sommarek, Jouko Vankka, Jaakko Ketola, Ilari Teikari and Kari Halonen - "A Digital Quadrature Modulator With On-Chip D/A Converter", European Solid-State Circuits, 2003, ESSCIRC '03, Conference on Sept. 16-18, 2003, Piscataway, NJ, USA, IEEE; Pages 85-88.
	Basuki Endah Priyanto, Choi Look Law, Yong Liang Quan - "Design & Implementation Of All Digital I-Q Modulator And Demodulator For High Speed WLAN in FPGA"; 2003 IEEE Pacific Rim Conference on Communications, Computers And Signal Processing, Victoria, BC Canada, Aug. 28-30, 2003; IEEE Pacific Rim Conference and Communications, Computers And Signal Processing, NY; Pages 659-662.
	European Search Report dated August 4, 2005

## **EXAMINER**

**DATE CONSIDERED**

\* Copies of these references are not enclosed Pursuant to 37 CFR 1.98(d). (See accompanying IDS)

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